

Scalasca Trace Tools

A scalable trace-based performance analysis toolset

Markus Geimer
Jülich Supercomputing Centre

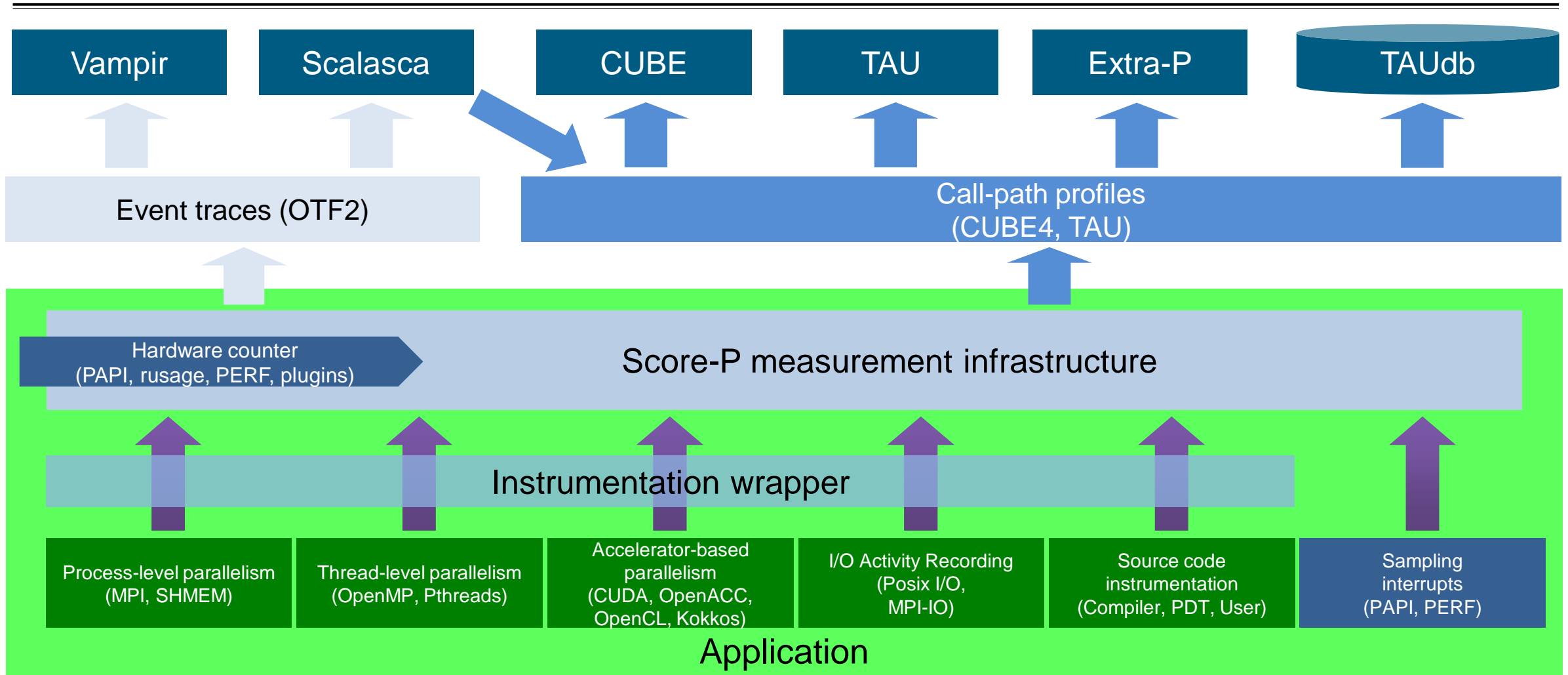
trace tools 
scalasca

Scalasca Trace Tools

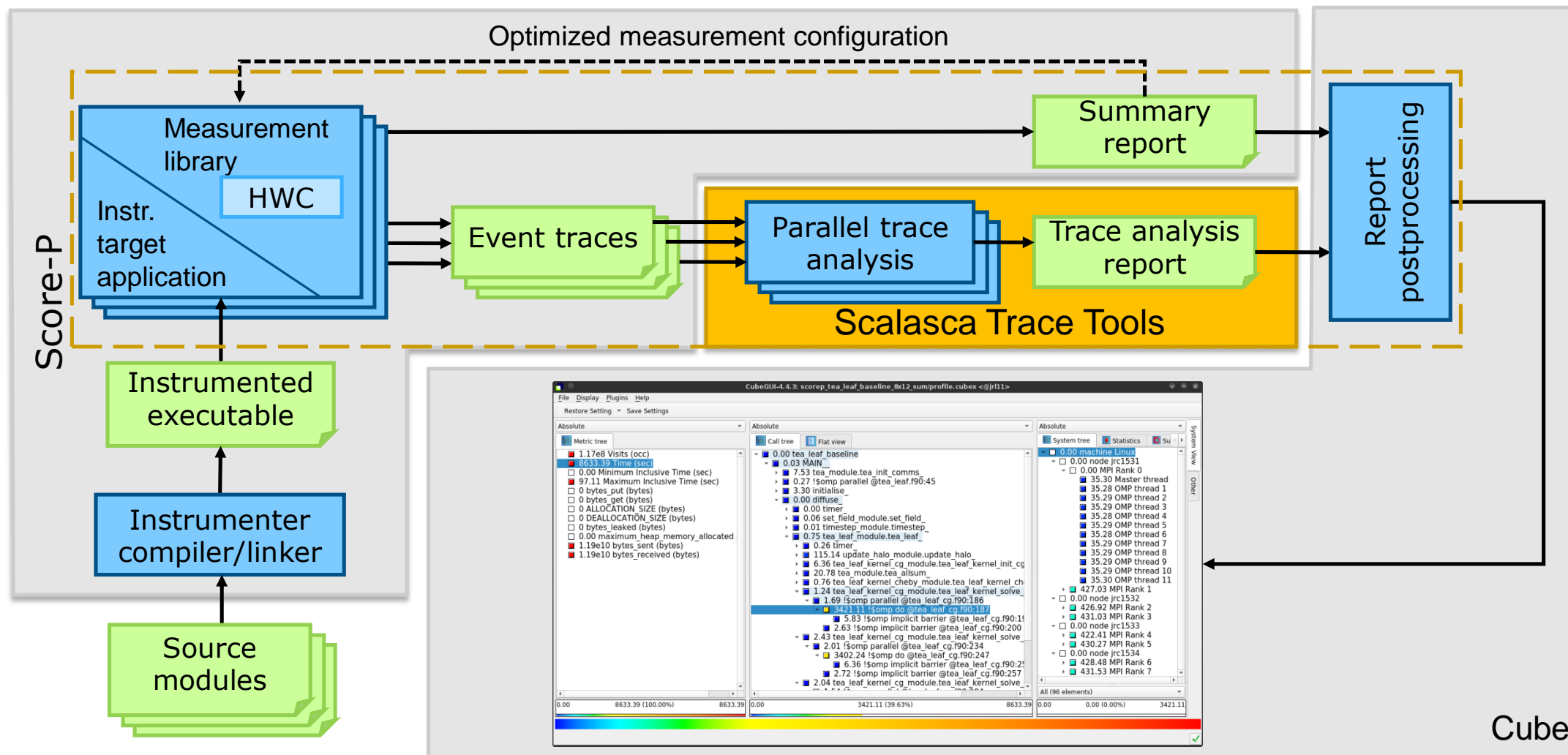
DOI [10.5281/zenodo.4700519](https://doi.org/10.5281/zenodo.4700519)

- **Scalable trace-based** performance analysis toolset for the most popular parallel programming paradigms
 - Current focus: MPI, OpenMP, and (to a limited extent) POSIX threads
 - Analysis of traces including only host-side events from applications using CUDA, OpenCL, or OpenACC (also in combination with MPI and/or OpenMP) is possible, but results need to be interpreted with some care
- Specifically targeting large-scale parallel applications
 - Demonstrated scalability up to 1.8 million parallel threads
 - Of course also works at small/medium scale
- Latest release:
 - Scalasca Trace Tools v2.6 (April 2021), coordinated with Score-P v7.0

Score-P Ecosystem Overview

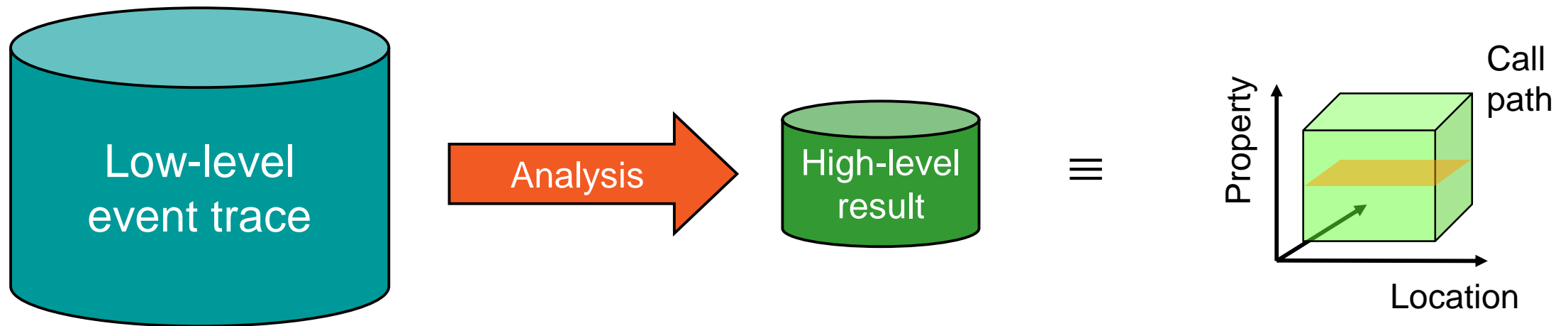


Scalasca Trace Tools Workflow



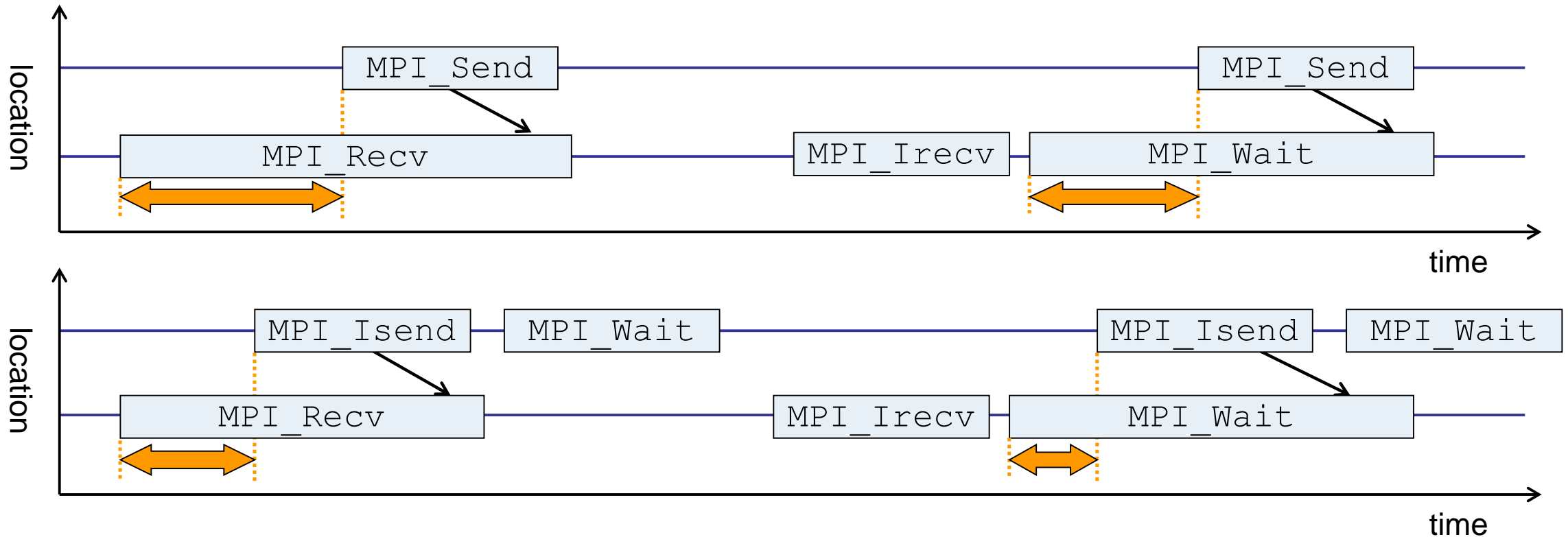
Automatic Trace Analysis

- Idea
 - Automatic search for patterns of inefficient behavior
 - Classification of behavior & quantification of significance
 - Identification of delays as root causes of inefficiencies



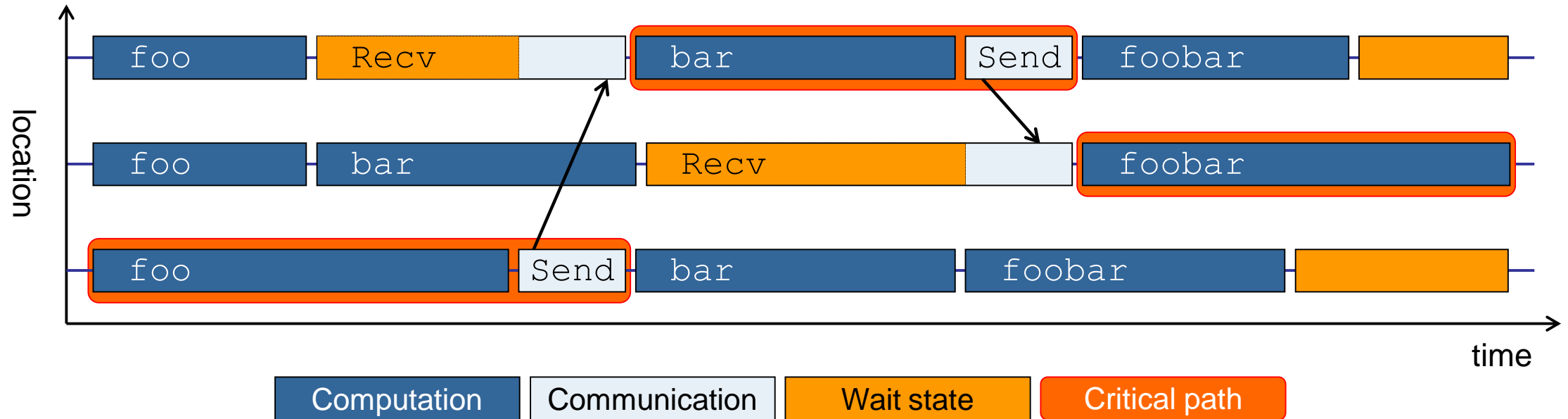
- Guaranteed to cover the entire event trace
- Quicker than manual/visual trace analysis
- Parallel replay analysis exploits available memory & processors to deliver scalability

Example: “Late Sender” Wait State



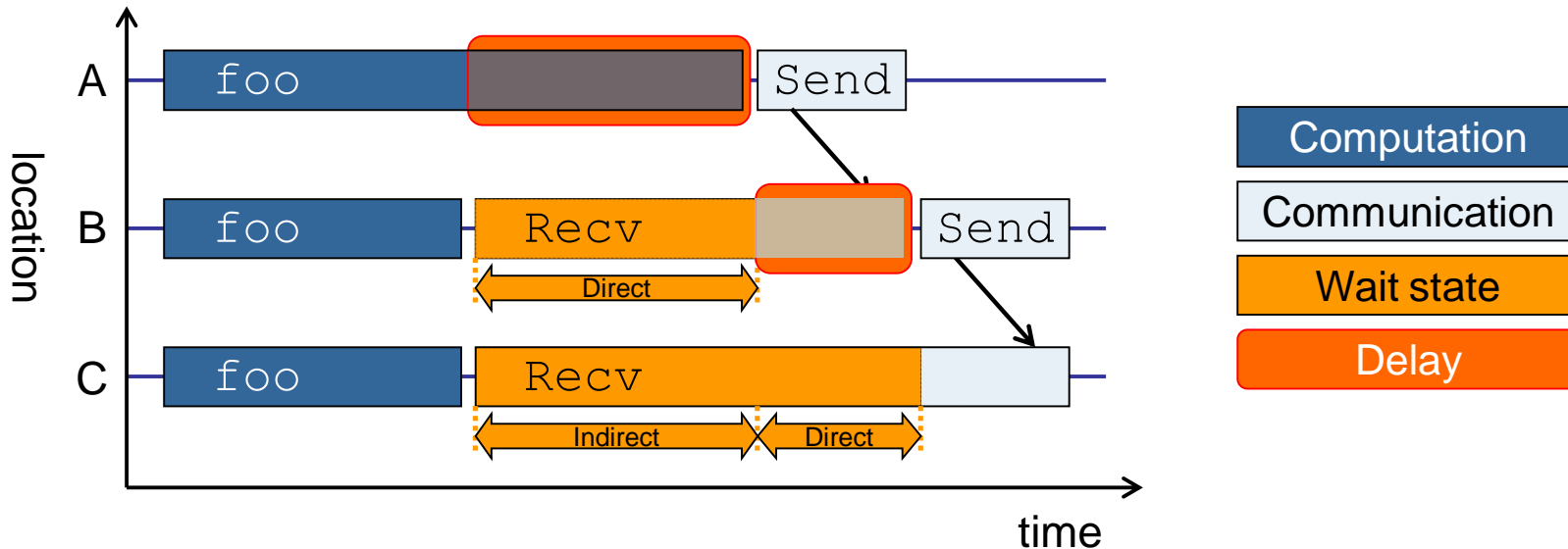
- Waiting time caused by a blocking receive operation posted earlier than the corresponding send
- Applies to blocking as well as non-blocking communication

Example: Critical Path



- Shows call paths and processes/threads that are responsible for the program's wall-clock runtime
- Identifies good optimization candidates and parallelization bottlenecks

Example: Root-cause Analysis



- Classifies wait states into direct and indirect (i.e., caused by other wait states)
- Identifies *delays* (excess computation/communication) as root causes of wait states
- Attributes wait states as *delay costs*

Objectives for DEEP-SEA

- Extend analysis capabilities
 - Add support for analyzing communication on MPI inter-communicators
- Improve MSA-awareness of analysis
 - Distinguish inter- and intra-module wait states
- Improve MSA-/MPMD-awareness of convenience commands
 - Support automatic invocation of trace analyzer in more cases

Scalasca Trace Tools: Further Information

- Collection of trace-based performance tools
 - Specifically designed for large-scale systems
 - Features an automatic trace analyzer providing wait-state, critical-path, and delay analysis
 - Supports MPI, OpenMP, POSIX threads, and hybrid MPI+OpenMP/Pthreads
- Available under 3-clause BSD open-source license

- Documentation & sources:
 - <https://www.scalasca.org>
- Contact:
 - mailto: scalasca@fz-juelich.de



Questions?